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STA	TEMENT	BY A	PPLICANT	First Named Inventor	Mohammad R. Mirabedini	
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(Use as many sheets as necessary)			ctessary)	Examiner Name	Not Yet Assigned Co Lena	
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) 1	Nobuyuki Sugii, Digh Hisamoto, Katsuyoshi Washio, Natsuki Yokoyama, and Shin'ichiro Kimura, "Enhanced Performance of Strained-Si MOSFETs on CMP SiGe Virtual Substrate," IEEE, 2001, 0-7803-7052-X/01, p. 1-4.	
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	2	FEOL Seminar 2002," October 29, 2002, p. 1-37.	
	3	K. Rim, S. Koester, M. Hargrove, J. Chu, P. M. Mooney, J. Ott, T. Kanarsky P. Ronsheim, M.leong, A. Grill, and HS. P. Wong, "Strained Si NMOSFE for High Performance CMOS Technology," IEEE 2001 Symposium on VLSI	r, Ts
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	4	Yee-Chia Yeo, Qiang Lu, Chenming Hu, Tsu-Jae King, T. Kawashima, M. Oi S. Mashiro, and J. Sakai, "Enhanced performance in sub-100 nm CMOSFI using strained epitaxial silicon-germanium", IEEE International Electron Device	Ts
		Meeting Technical Digest, pp. 753-756, San Francisco, CA, Dec. 2000, www.eecs.berkeley.edu/IPRO/Summary/01abstracts/ycyeo.1.html, p. 1-4.	
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	6	Akira Yamada, Tatsuro Watahiki, Shuhei Yagi, Katsuya Abe, and Makoto Kon: "Epitaxial Growth of Strained Si _{1-x} C _x on Si and Its Application to MOSFET," International Symposium on Quantum Effect Electronics, 2002.	agai,
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